

Figure 19

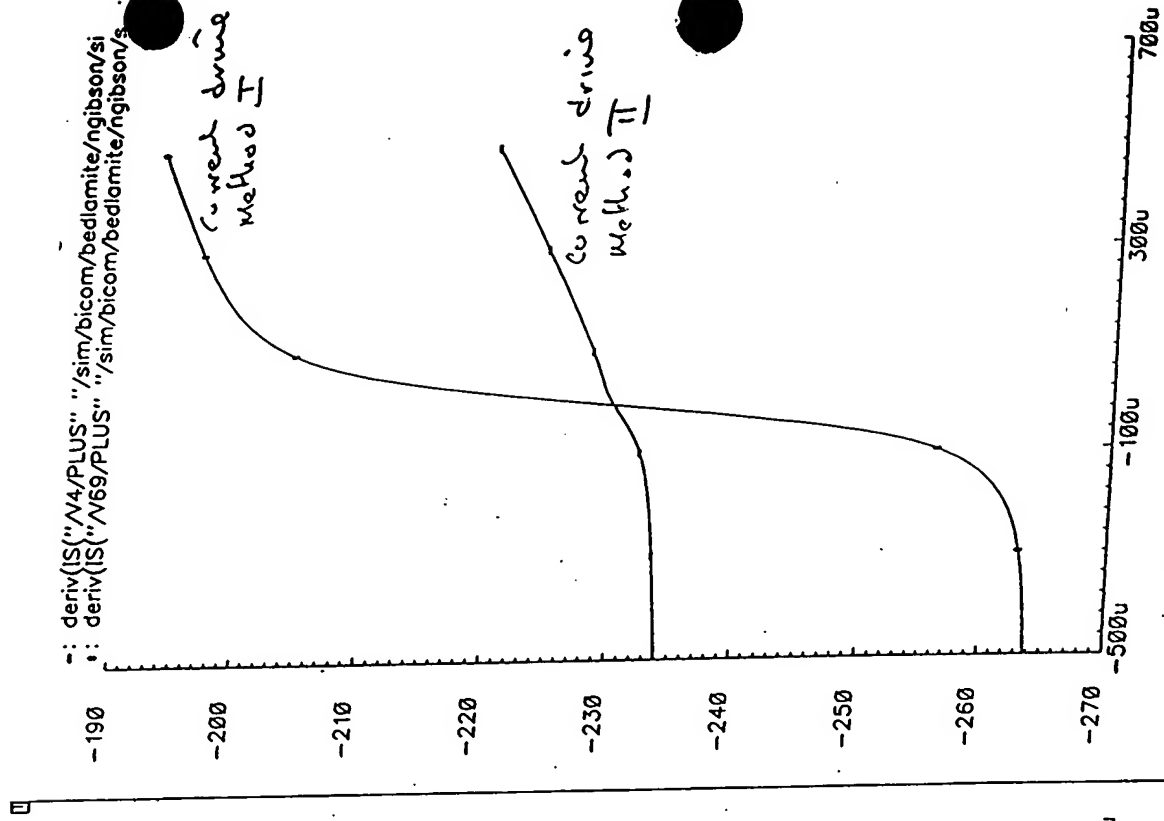


Figure 21

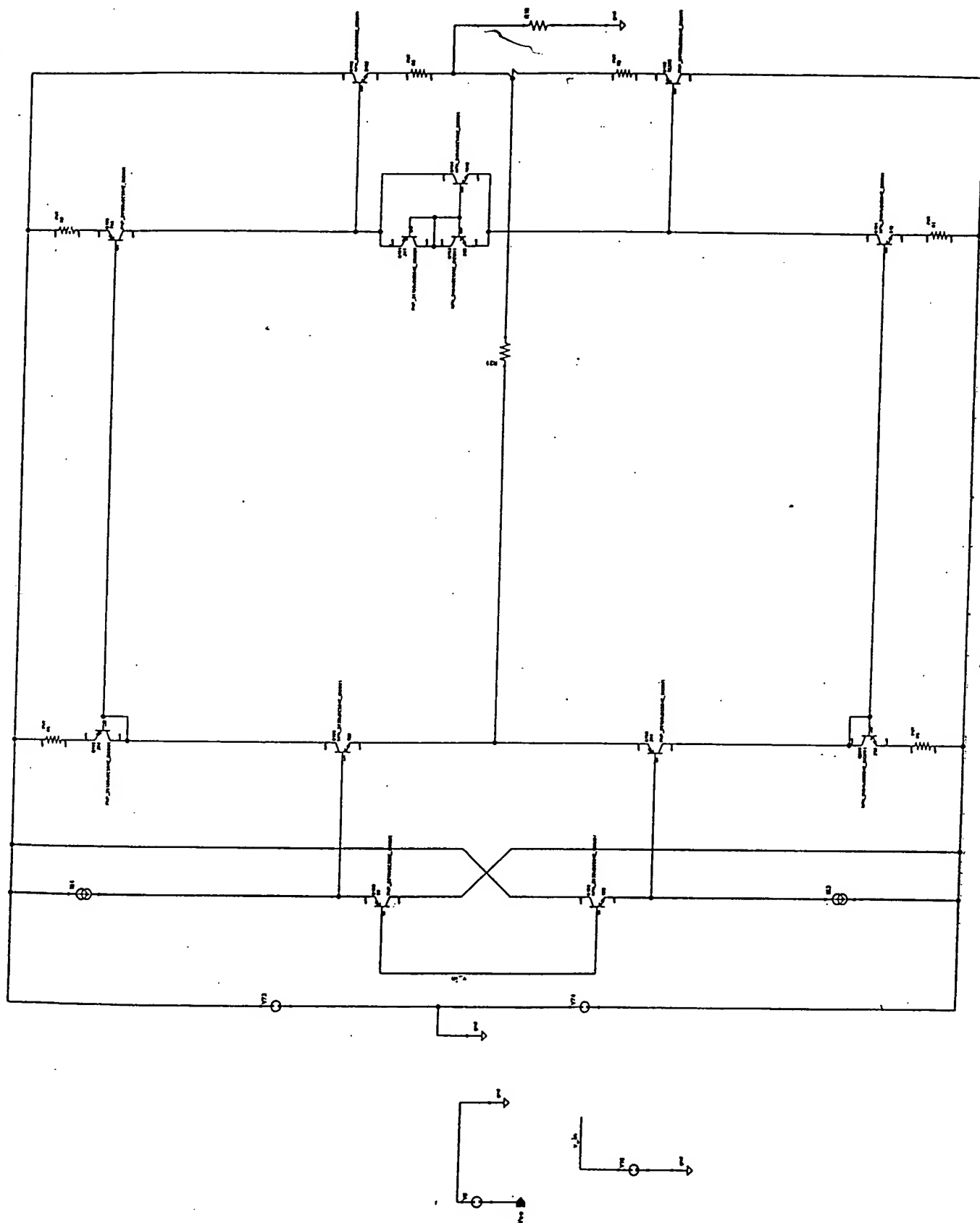


Figure 1 (Prior Art)

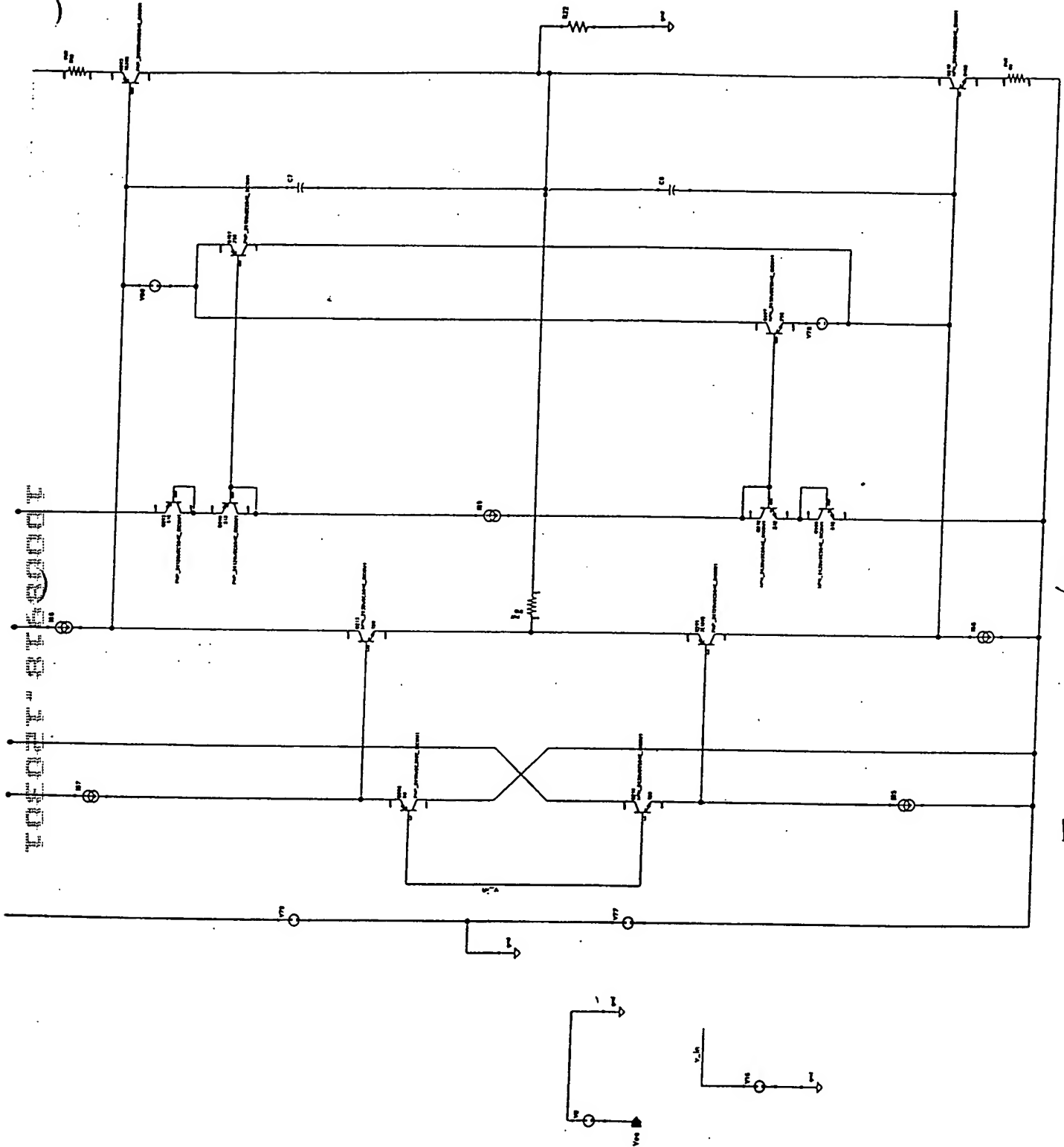
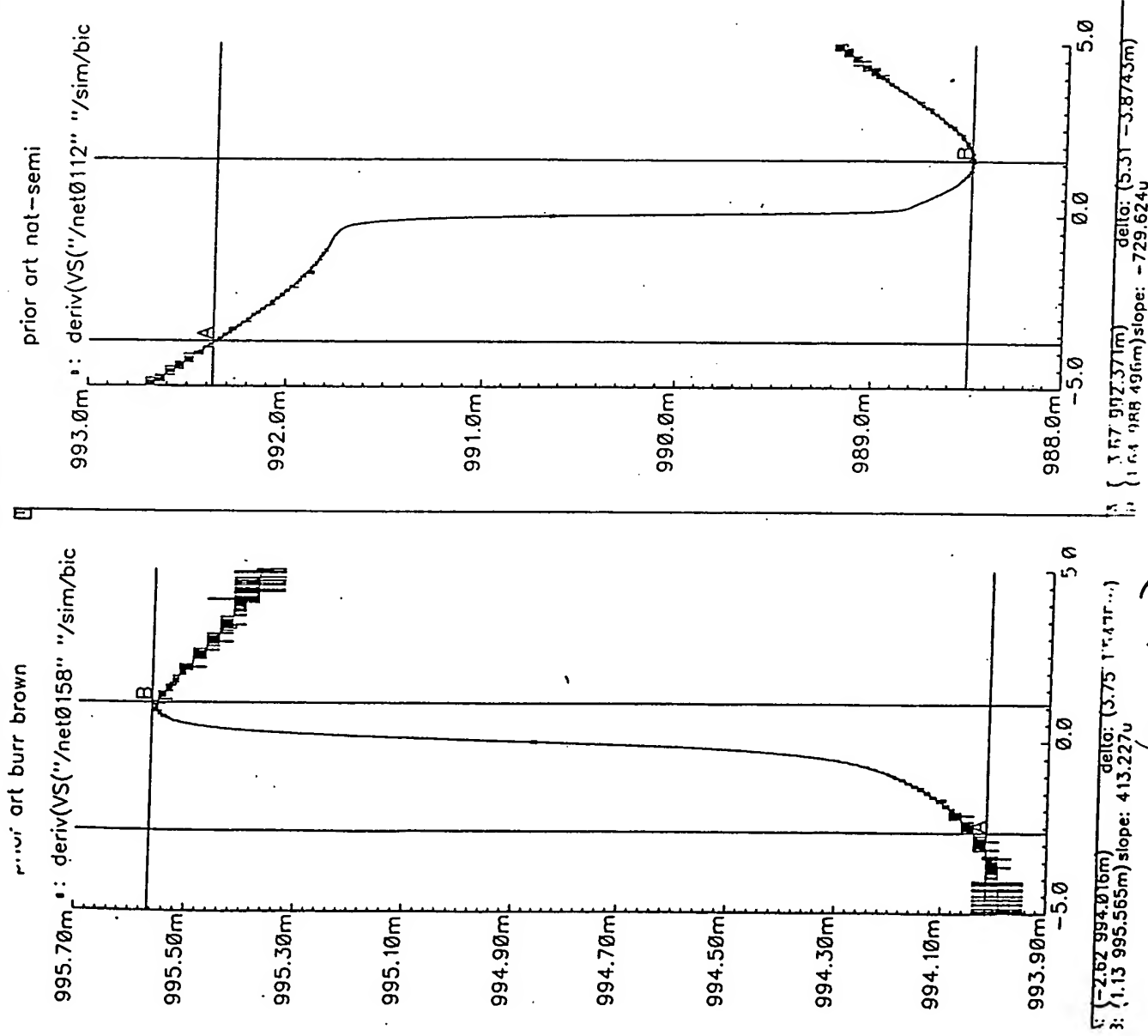


Figure 2 (Prior Art)



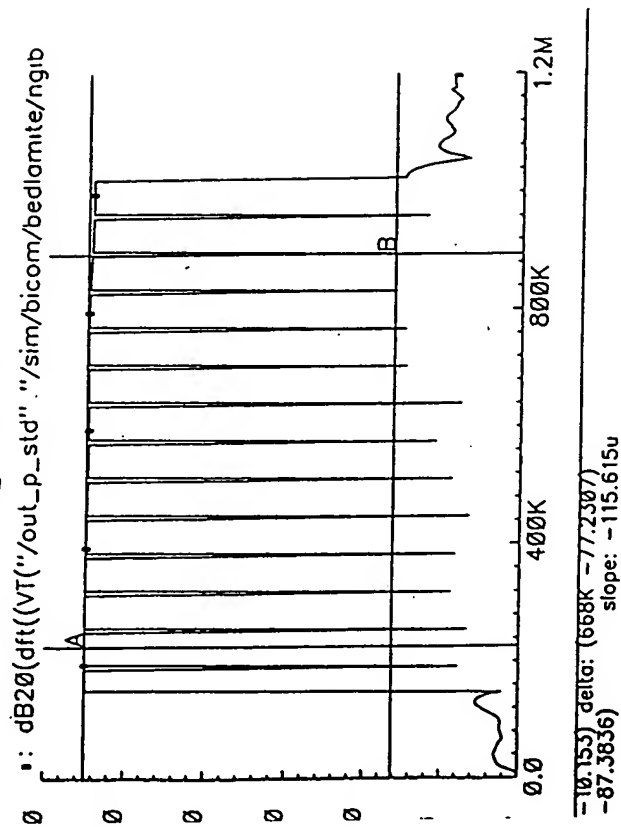
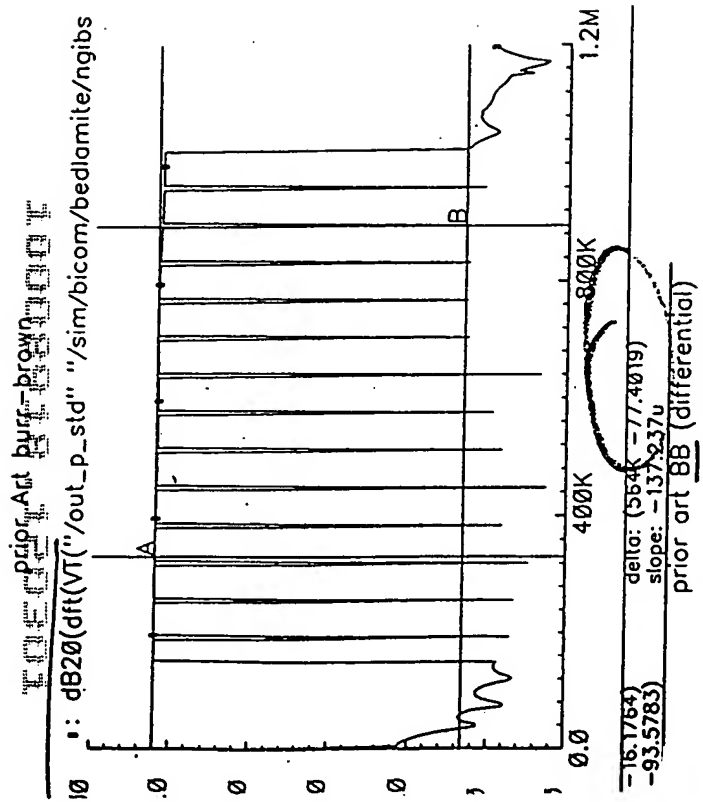


Figure 5 (Prior Art)

Foot 8768000

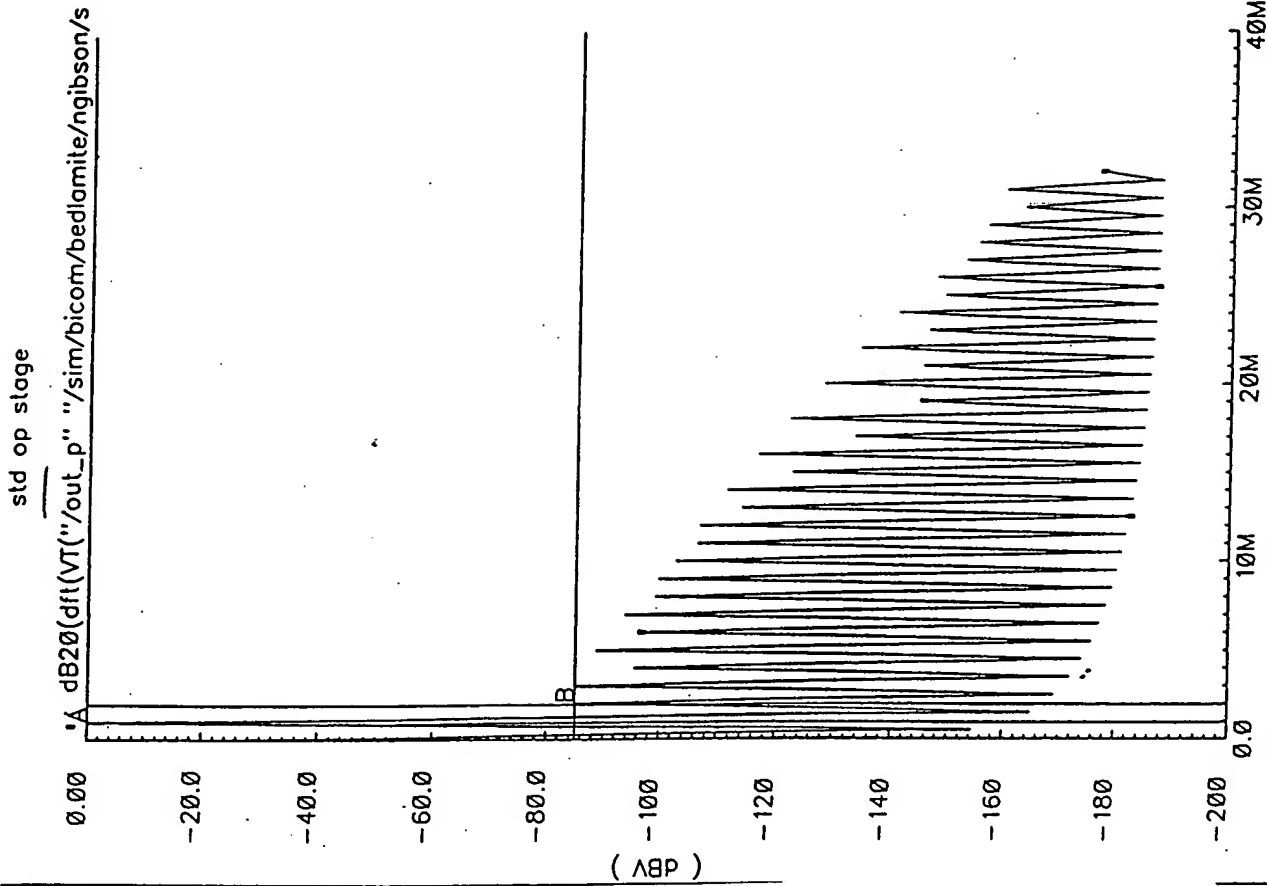


Figure 6 (Prior Art)

10008918-120301

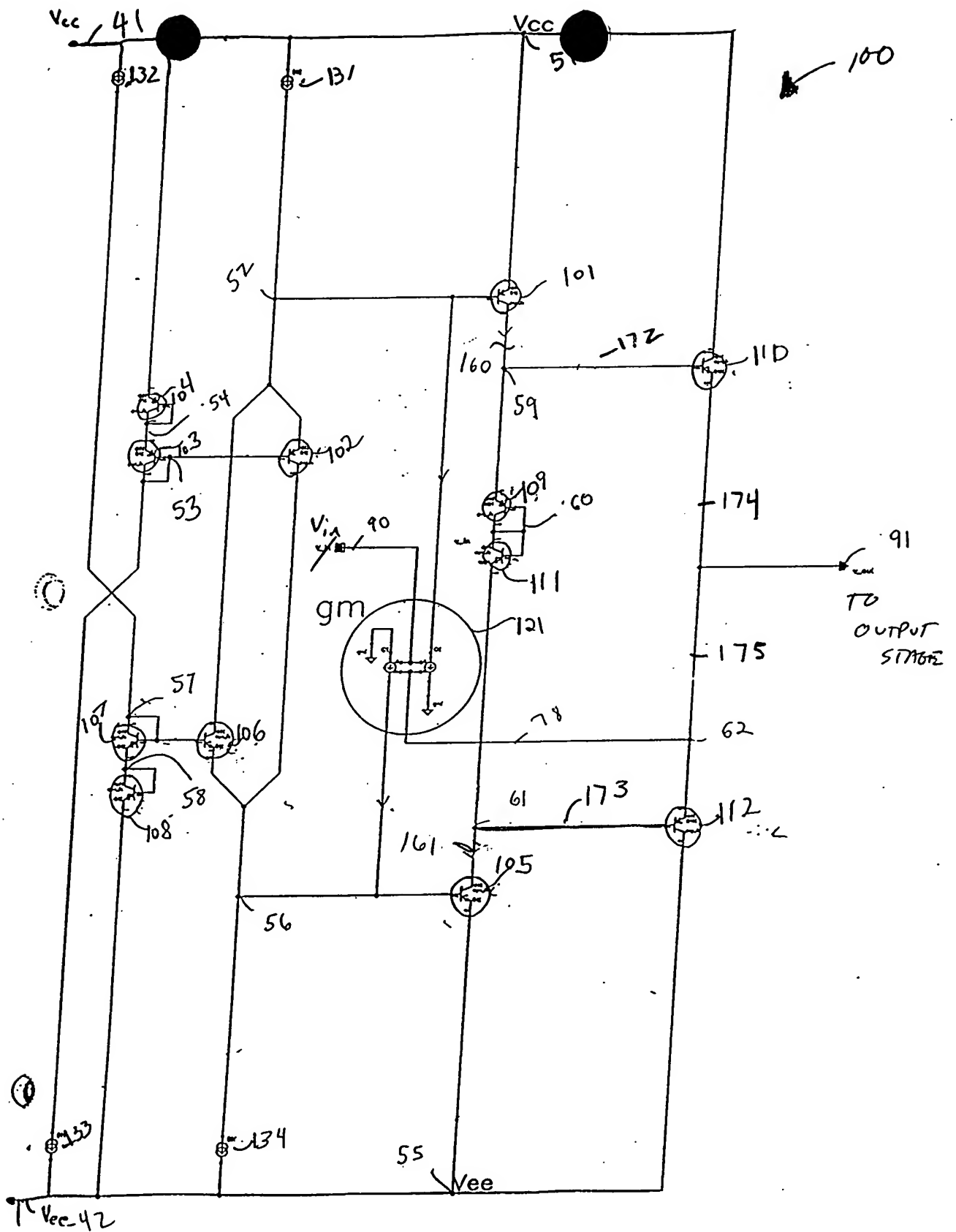


Figure 7

10088918.120301

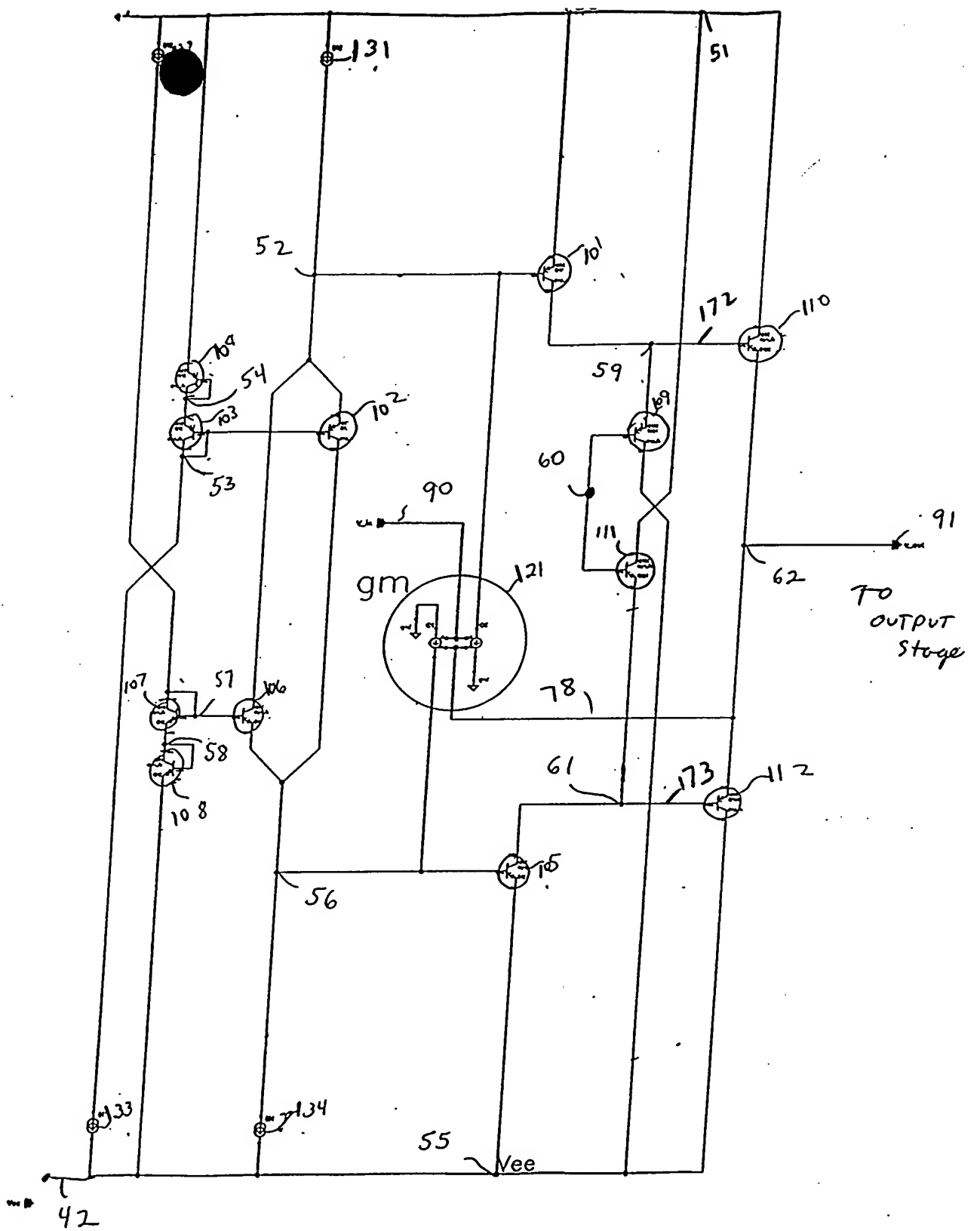


Figure 8

Plot of "846000"

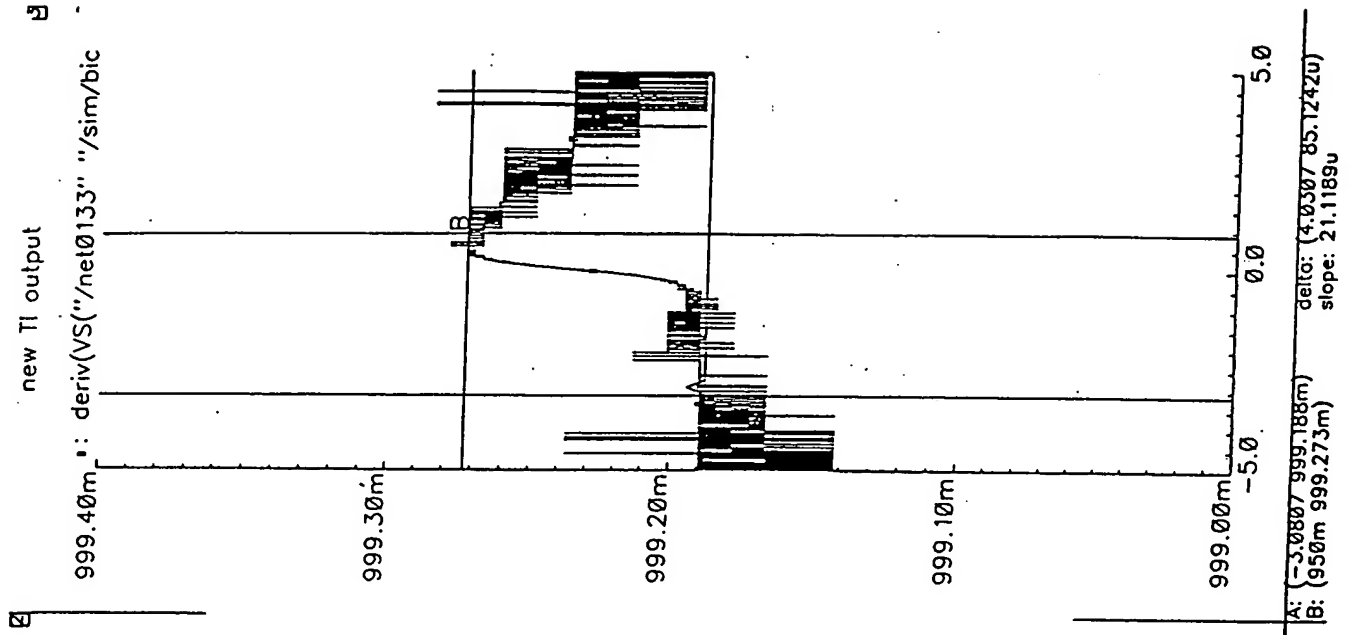


Figure 9

new hybrid approach 63000

dB20(dft(VT("/out_p_new" "/sim/bicom/bedlamite/ngibs

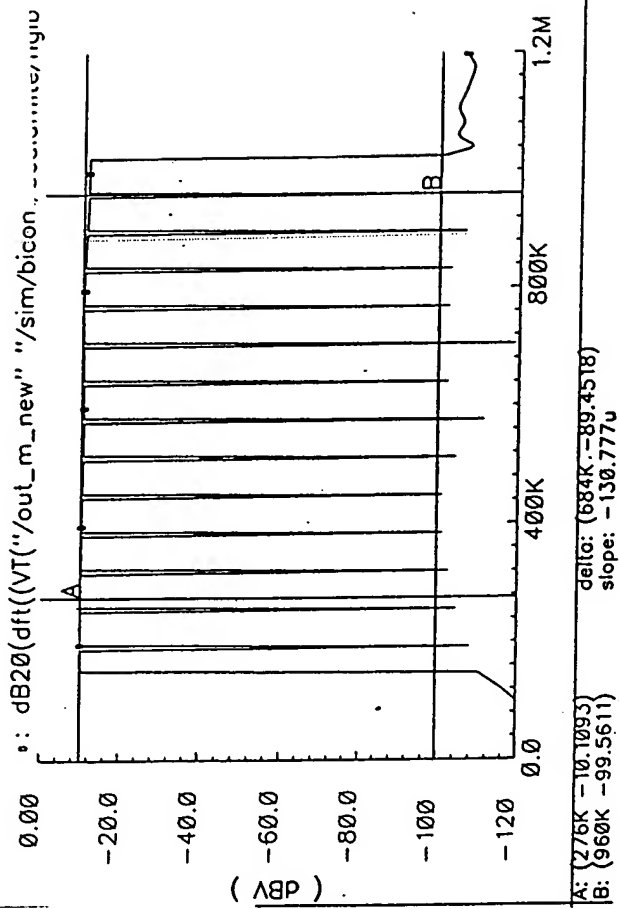
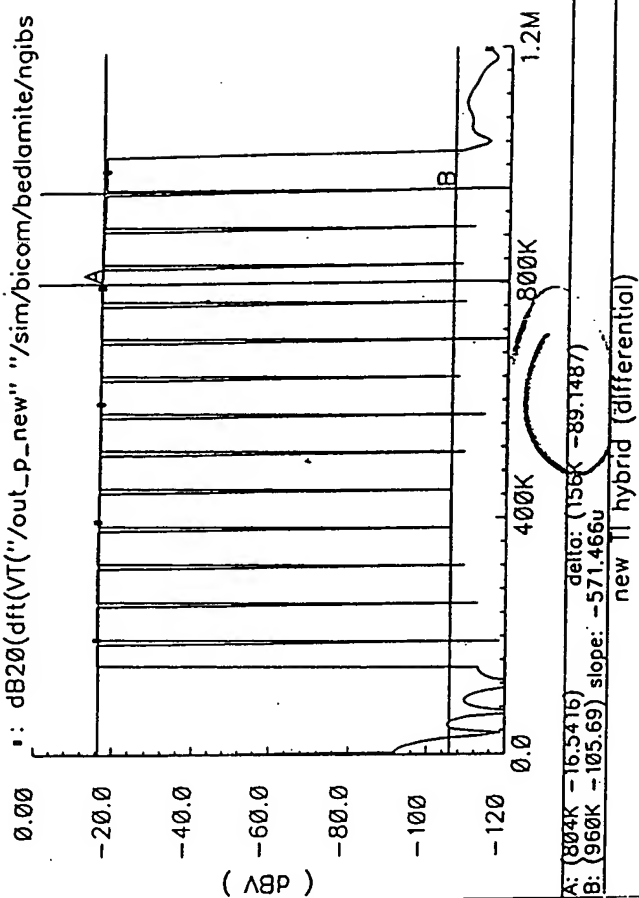


Figure 10

Report 31590001
new up stage

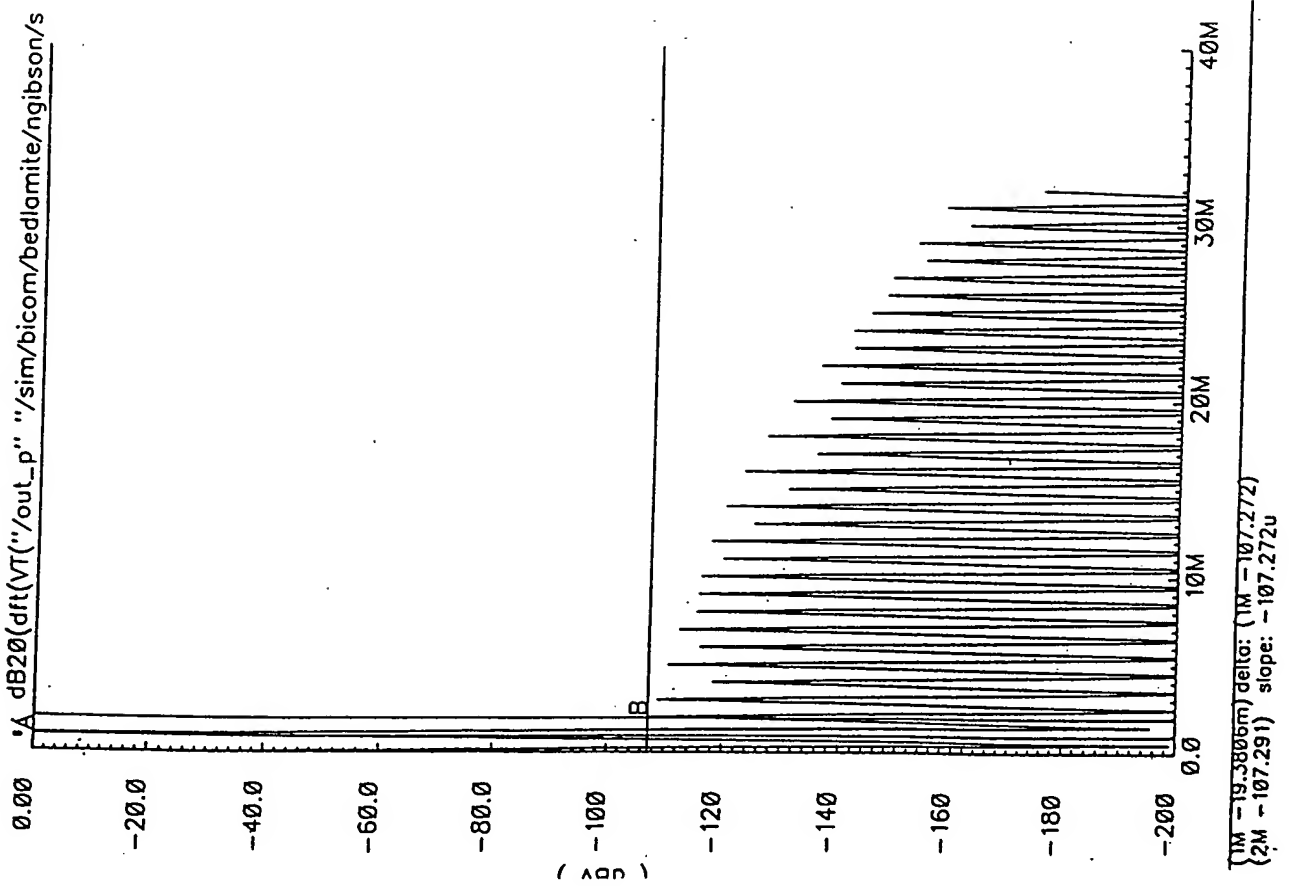


Figure 11

[illegible]

Figure 12

Figure 13

10003918-120301

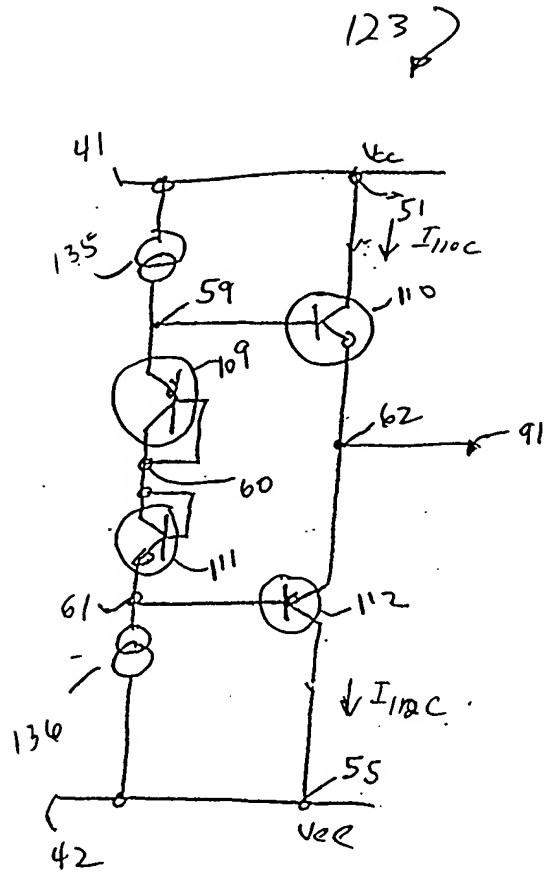


Figure 14

123)

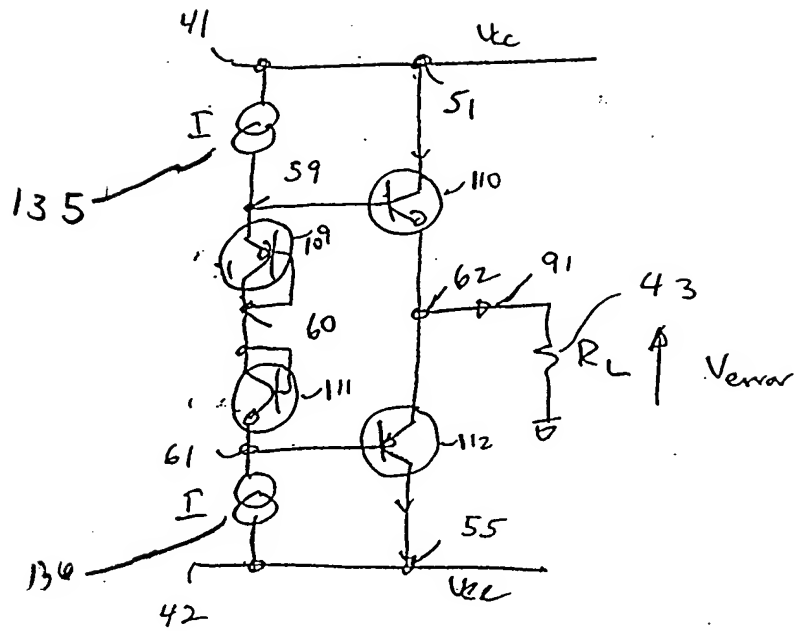


Figure 15

A graph showing the relationship between β_n (vertical axis) and $\delta \sigma_{12}$ (horizontal axis). The curve starts at a constant value for negative $\delta \sigma_{12}$, then increases sigmoidally as $\delta \sigma_{12}$ becomes positive, eventually reaching a higher constant value. The horizontal axis is labeled $\delta \sigma_{12}$ with a '+' sign on the right and a '-' sign on the left. The vertical axis is labeled β_n .

Figure 16

10003518-120301

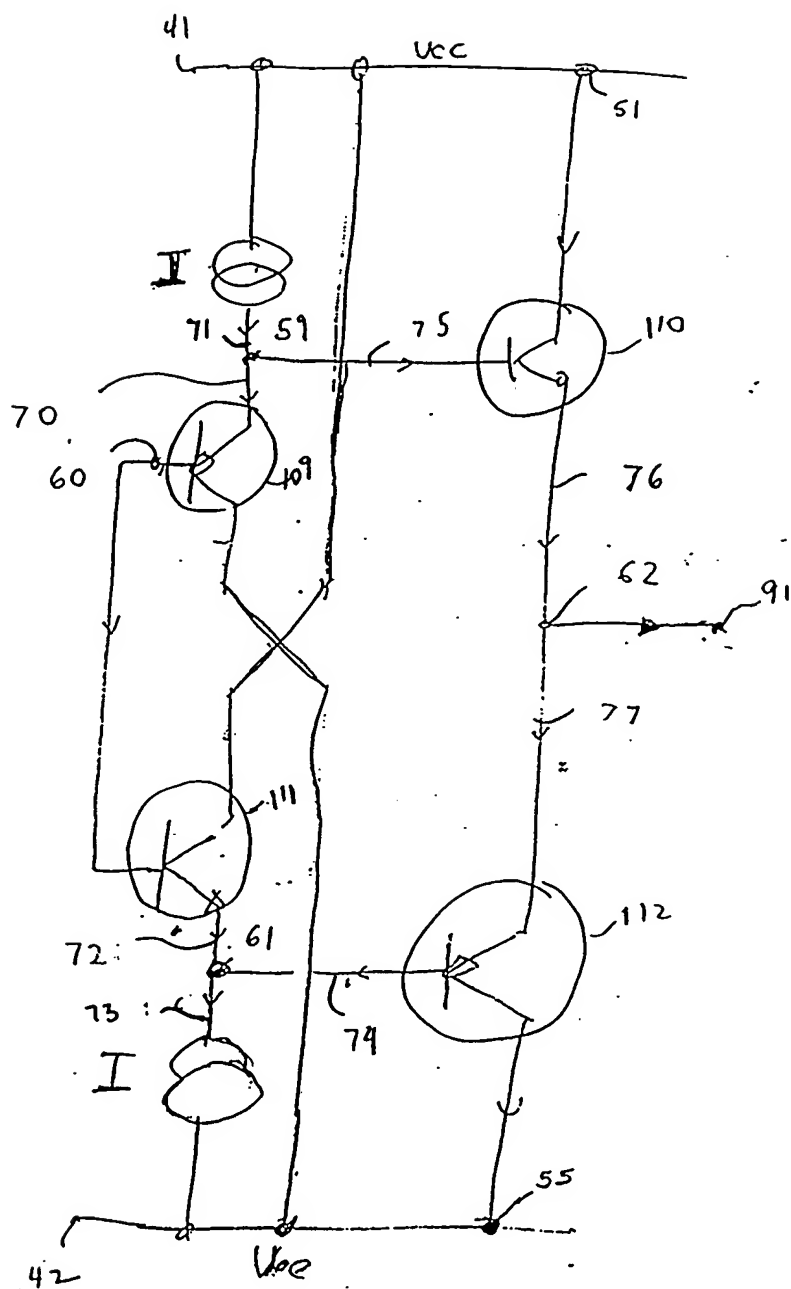


Figure 17

10008918-120301

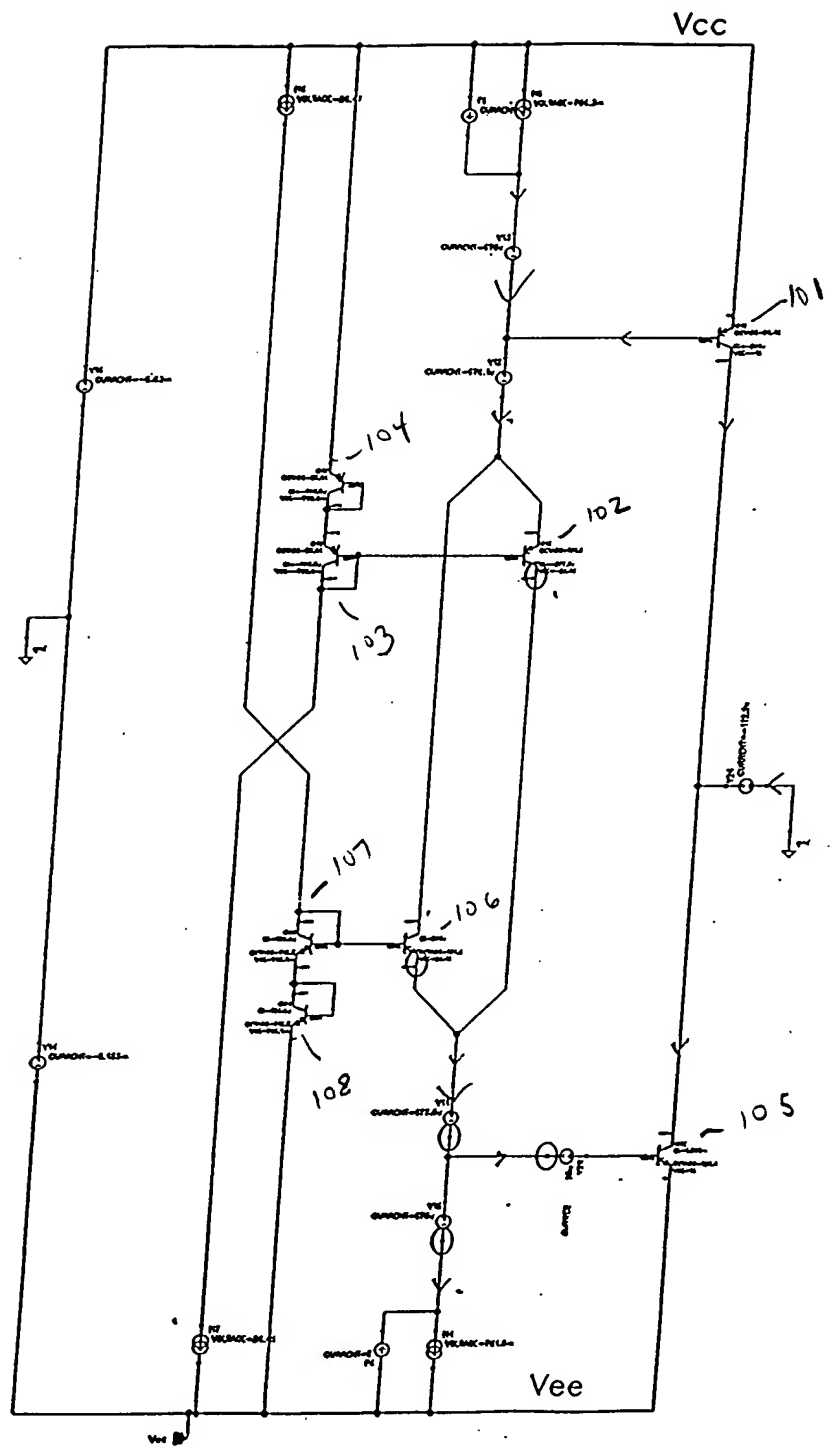
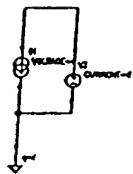


Figure 22

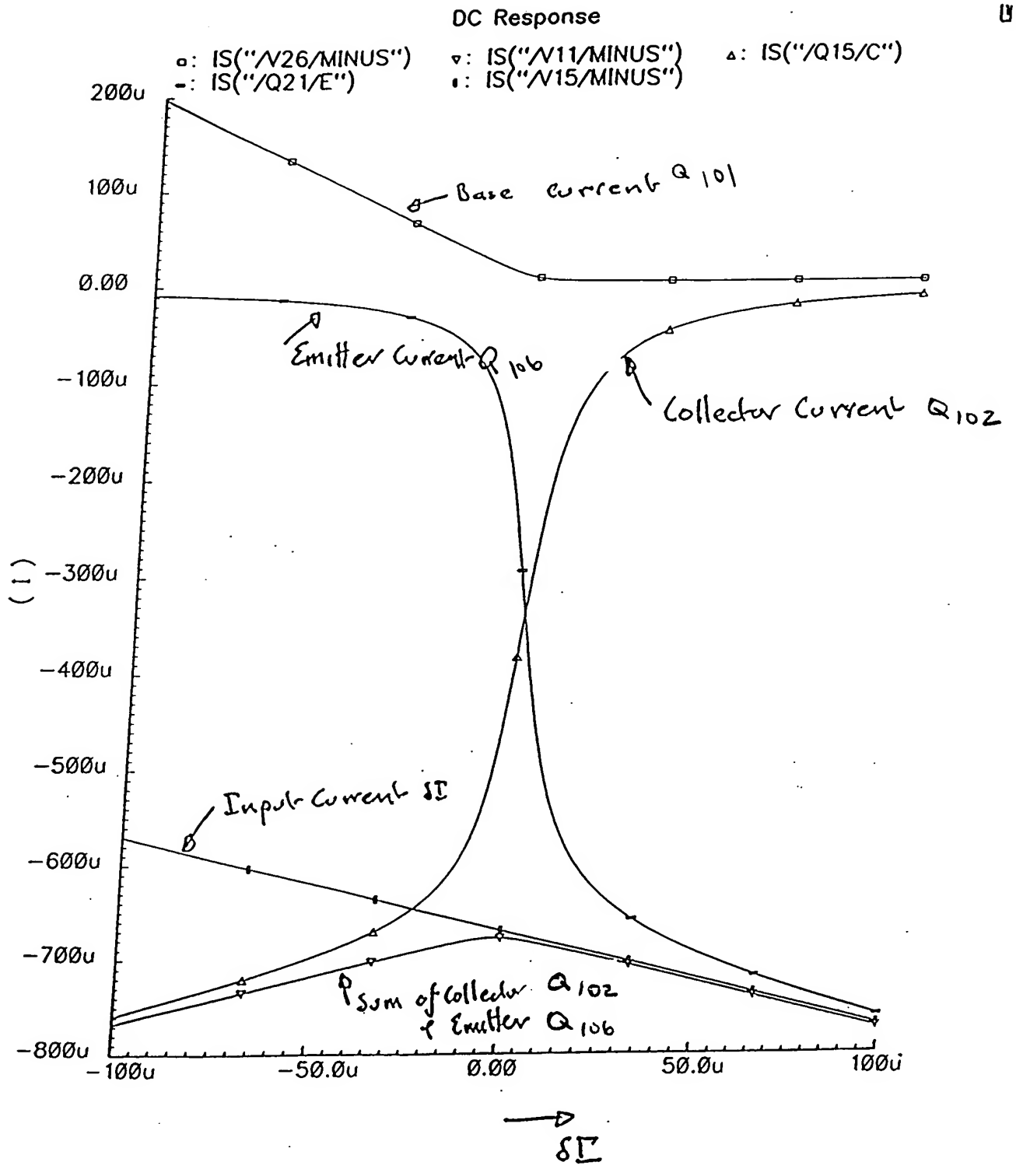
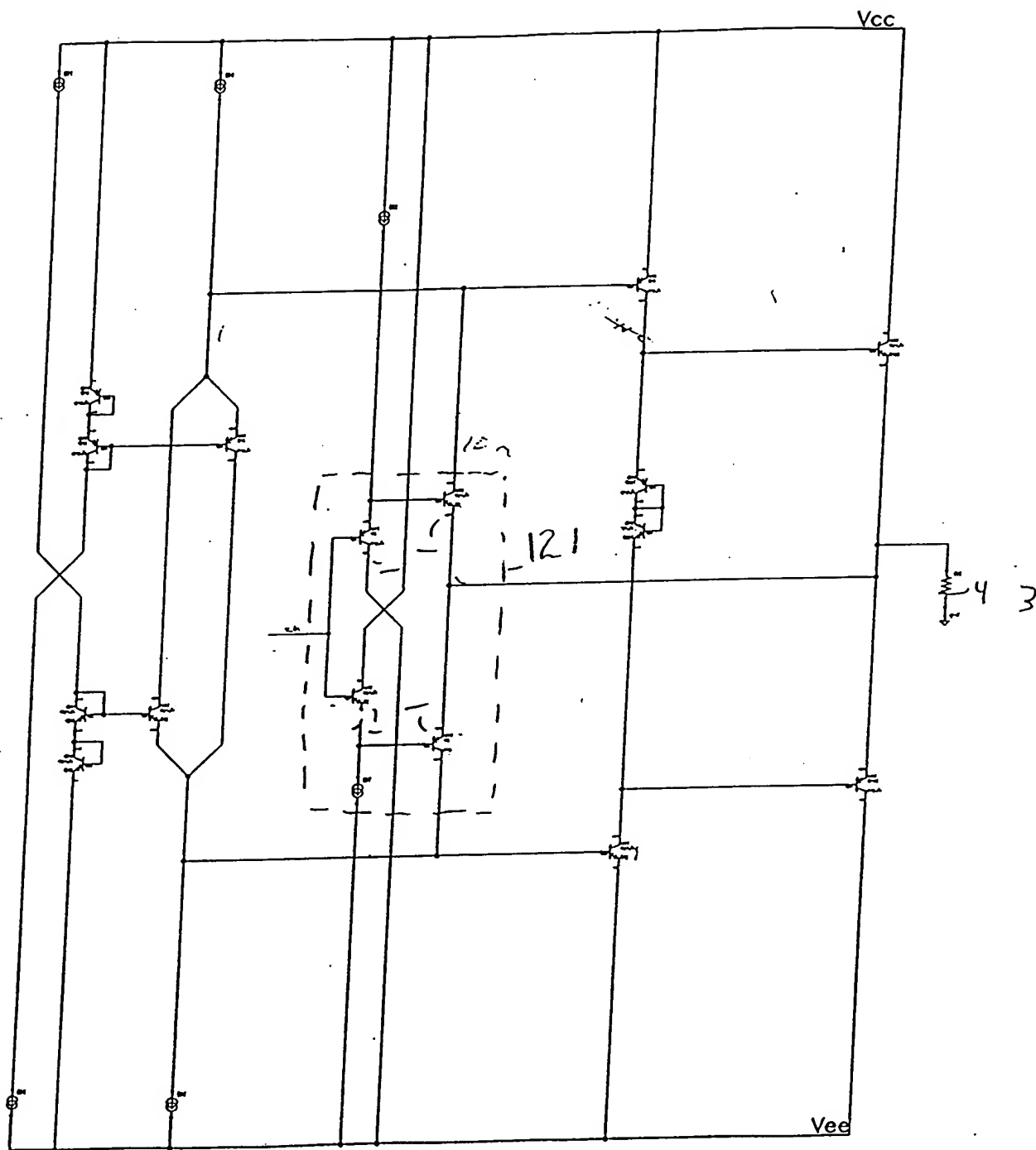


Figure 23

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10003943-123334

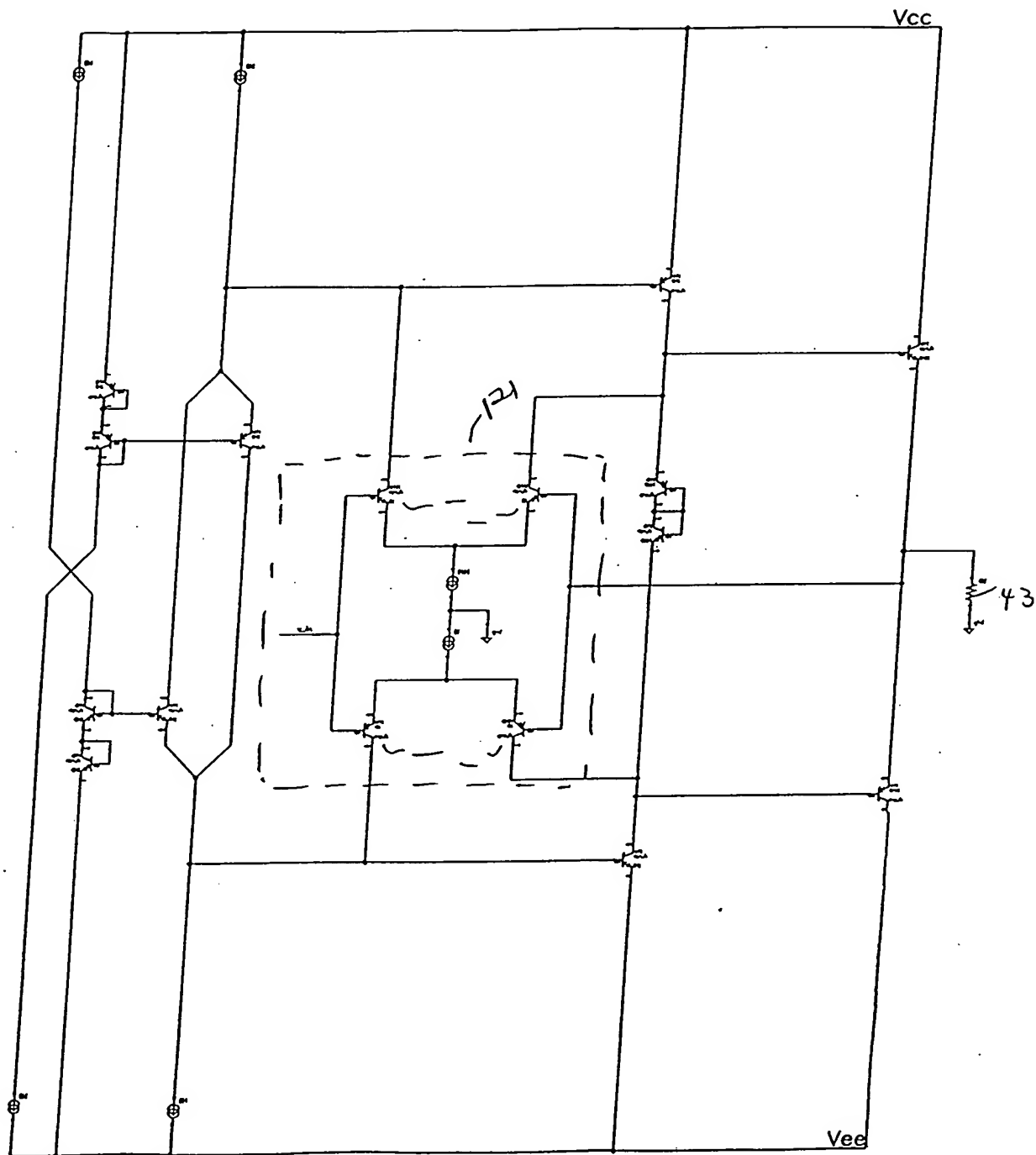


Figure 25

The diagram illustrates a 4-bit ripple-carry adder circuit. It is powered by V_{cc} and V_{ee} rails. The circuit consists of several stages of transistors, with a central section labeled "1/21" enclosed in a dashed box. This section likely represents a full adder core. The inputs are labeled 1, 2, 3, and 4, and the carry-in is labeled "43". The output is taken from a node between two transistors in the carry chain. The circuit uses a combination of NMOS and PMOS transistors to implement the logic functions for each bit position.

Figure 26

10008948-420304

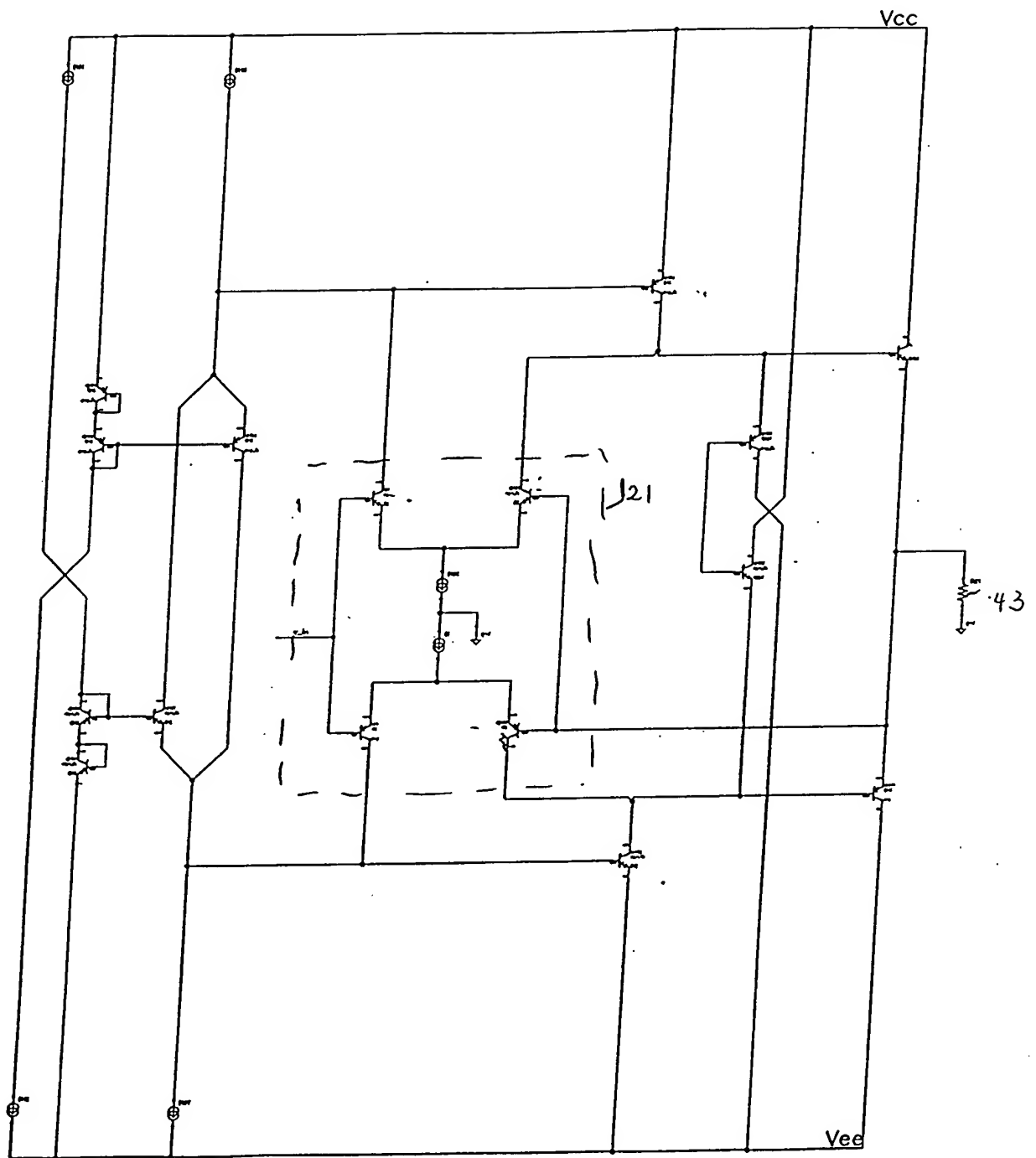


Figure 27

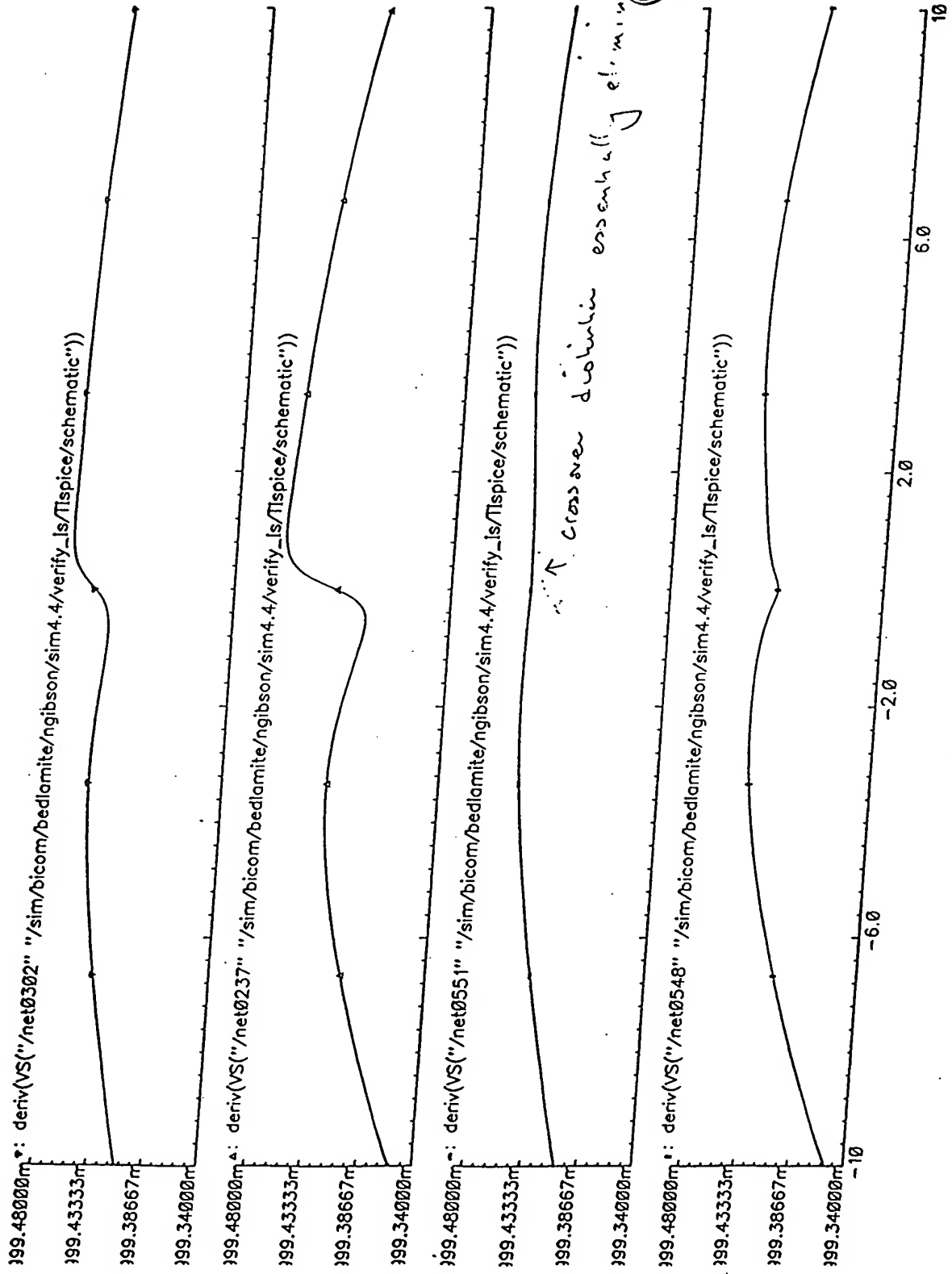


Figure 28

1000339-1000

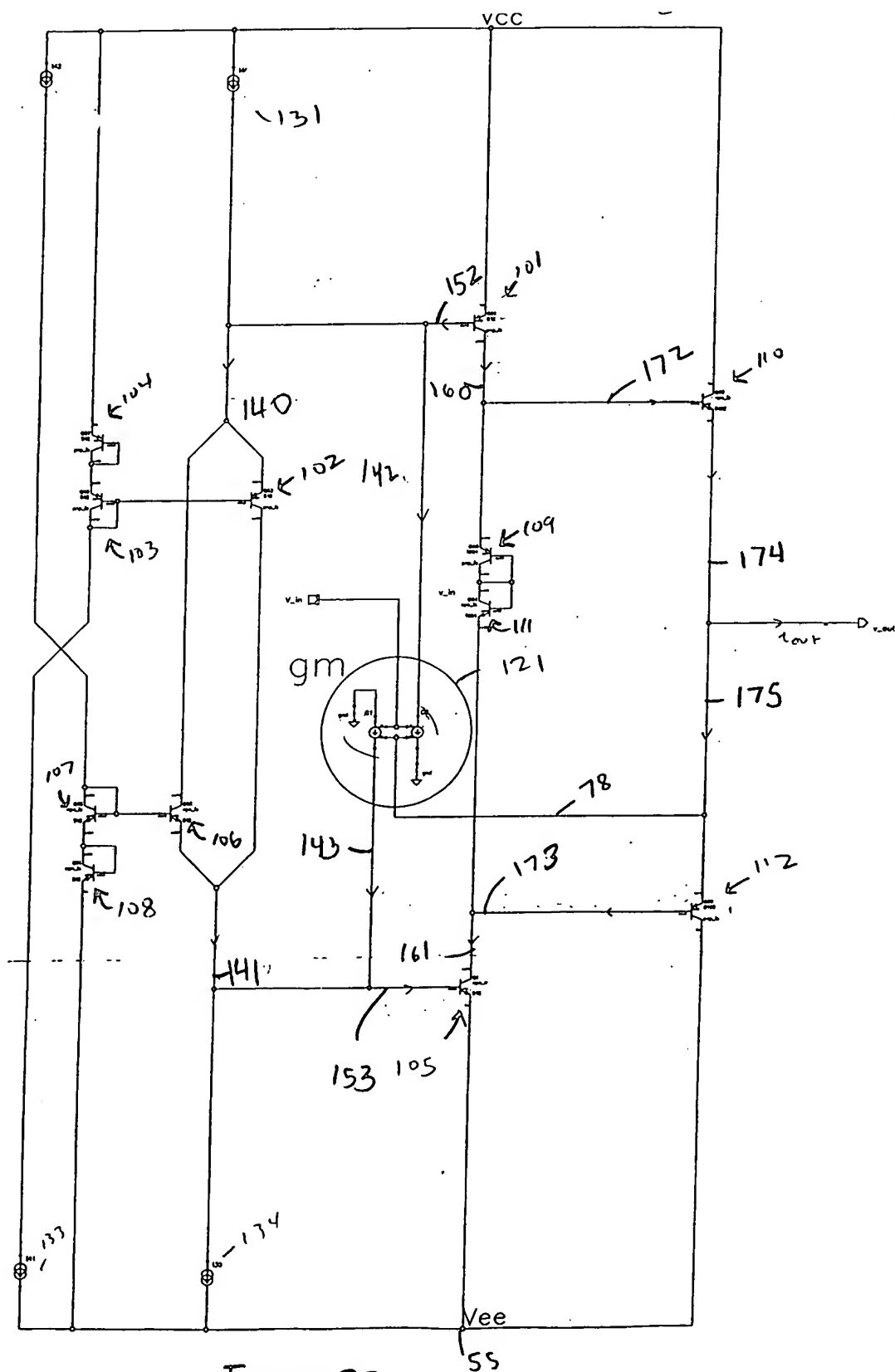


Figure 30

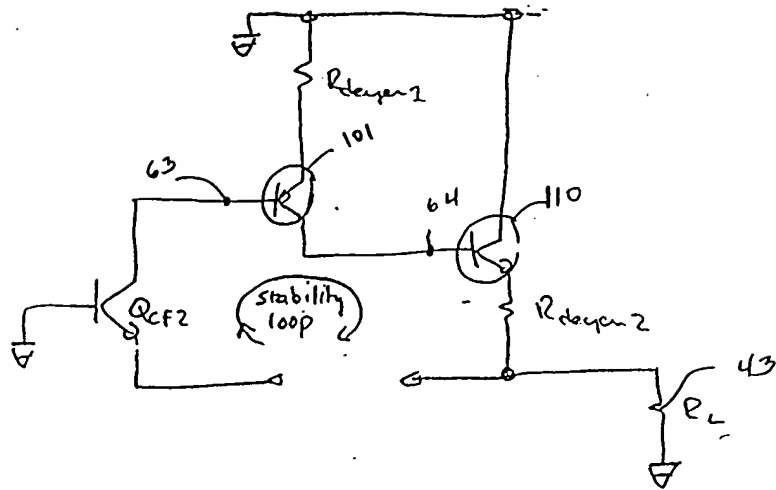


Figure 31

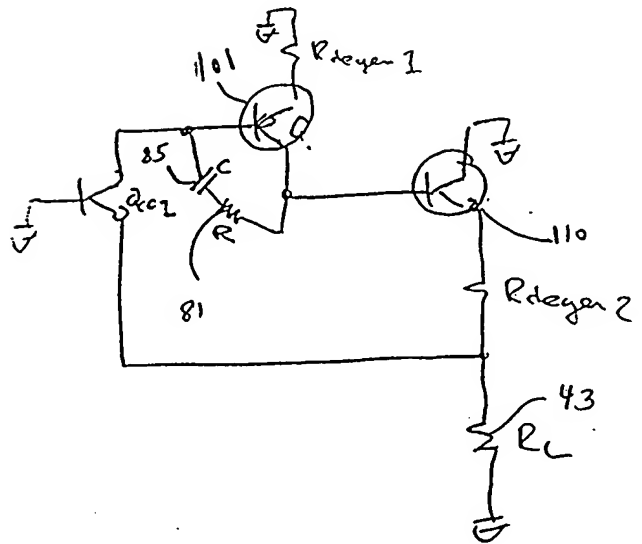


Figure 32

Figure 34

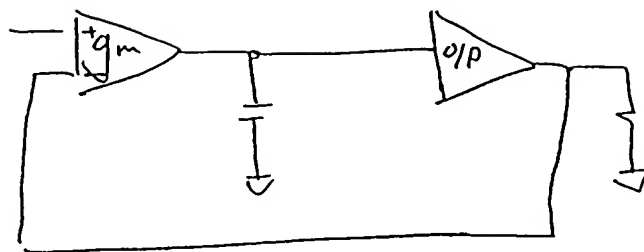


Figure 35